

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (Currently Amended) A device for receiving a stream of data, said device comprising:

identifying means for identifying a first plurality of portions of data from said received stream of data and producing a first output stream;

first output means for outputting said first output stream;

selecting means for selecting a second plurality of portions of data from said received stream of data and producing an alternative output stream;

determining means for determining the relative timing of said second plurality of portions of data; and

second output means for outputting said alternative output stream, wherein the first plurality of portions of data includes audio and video data and the relative timing between portions of data in the received stream of data and in the alternative output stream is maintained.

2. (Previously Presented) The device as claimed in claim 1, wherein said stream of data comprises a plurality of data packets and said plurality of portions of data occur within a packet.

3. (Previously Presented) The device as claimed in claim 2, wherein each portion of data comprises a byte of data.

4. (Previously Presented) The device as claimed in claim 2, wherein means are provided for identifying which of said plurality of data packets comprise data to be output by said output means.

5. (Previously Presented) The device as claimed in claim 1, wherein storage means are provided for storing information for each portion of a packet indicating if the portion of data is valid or invalid.

6. (Previously Presented) The device as claimed in claim 5, wherein said information comprises a data portion valid signal.

7. (Previously Presented) The device as claimed in claim 5, wherein the storage means comprises a first-in-first-out buffer.

8. (Previously Presented) The device as claimed in claim 2, wherein each data packet includes information identifying the beginning of said packet and means are provided for identifying the beginning of each packet.

9. (Previously Presented) The device as claimed in claim 8, wherein said means for identifying the beginning of a packet provides an output for controlling the timing of the output of the selected data by said output means.

10. (Previously Presented) The device as claimed in claim 9, wherein a fixed latency is provided between the input plurality of portions of data received by the device and the output of those selected portions of data.

11. (Previously Presented) The device as claimed in claim 1, wherein means are provided for storing the selected portions of said data.

12. (Previously Presented) The device as claimed in claim 11, wherein the means for storing the selected portions of data stores only the selected portions of data.

13. (Previously Presented) The device as claimed in claim 11, wherein the means for storing the selected portions of data is a first in first out buffer.

14. (Previously Presented) The device as claimed in claim 4 wherein means are provided for storing the selected portions of said data and the second output means comprises a state machine which controls the output of the selected portions of data, said state machine receives outputs from said means for storing said selected portions of data, and said means for storing information on each portion of data.

15. (Previously Presented) The device as claimed in claim 1, wherein the input stream conforms to the MPEG-2 standard.

16. (Currently Amended) A digital video device incorporating a device for receiving a stream of data, said device comprising:

identifying means for identifying a first plurality of portions of data from said received stream of data and producing a first output stream;

first output means for outputting said first output stream;

selecting means for selecting a second plurality of portions of data from said received stream of data and producing an alternative output stream;

determining means for determining the relative timing of said second plurality of portions of data; and

second output means for outputting said alternative output stream, wherein the first plurality of portions of data includes audio and video data and the relative timing between portions of data in the received stream of data and in the alternative output stream is maintained.

17. (Currently Amended) A method of processing a stream of data comprising the steps of:

receiving a stream of data;

identifying a first plurality of portions of data from said received stream of data and producing a first output stream;

outputting said first output stream;

selecting a second plurality of portions of data from said received stream of data and producing an alternative output stream;

determining the relative timing of said second plurality of portions of data; and

outputting the alternative output stream, wherein the first plurality of portions of data includes audio and video data and the relative timing between portions of data in the received stream of data and in the alternative output stream is maintained.

18. (Previously Presented) The method of claim 17 wherein the received data stream comprises multiplexed portions of the first plurality of portions of data and the second plurality of portions of data.

19. (Currently Amended) The method of claim 17 wherein the first plurality of portions of data comprises audio and video data and the second plurality of portions of data comprises audio and video data.

20. (Canceled).

21. (Currently Amended) The device of claim 20-1 wherein the second plurality of portions of data includes audio data and video data.

22. (Currently Amended) A device for receiving a stream of data, the device comprising:

first processing circuitry for identifying a first plurality of portions of data in the received data stream and producing a first output data stream;

second processing circuitry for identifying a second plurality of portions of data in the received data stream and producing a second output data stream; and

timing control circuitry for maintaining relative timing between portions of data in the received data stream and portions of data in the second output stream, wherein the timing control circuitry comprises a state machine.

23. (Canceled).

24. (Previously Presented) The device of claim 22 wherein the first plurality of portions of data comprises audio and video data.

25. (Previously Presented) The device of claim 24 wherein the second plurality of portions of data comprises audio and video data.

26. (Currently Amended) A device for receiving a stream of data, the device comprising:

identifying circuitry for identifying a first plurality of portions of data in the received data stream and a second plurality of portions of data in the received data stream;

first output circuitry for producing a first output stream corresponding to the first plurality of portions of data in the received data stream;

second output circuitry for producing a second output stream corresponding to the second plurality of portions of data in the received data stream; and

timing control circuitry coupled to the second output circuitry for maintaining relative timing between the received data stream and the second output stream, wherein the timing control circuitry comprises a state machine.

27. (Canceled).

28. (Previously Presented) The device of claim 26 wherein the first output stream comprises audio and video data.

29. (Previously Presented) The device of claim 28 wherein the second output stream comprises audio and video data.

30. (New) A device for receiving a stream of data, the device comprising:
an input interface for identifying a first plurality of portions of data in the received data stream and a second plurality of portions of data in the received data stream;
an output interface for producing a first output stream corresponding to the first plurality of portions of data in the received data stream and a second output stream corresponding to the second plurality of portions of data in the received data stream; and
a timing controller coupled to the output interface for maintaining relative timing between the received data stream and the second output stream, wherein the first plurality of portions of data includes audio and video data.

31. (New) The device of claim 30 wherein the timing controller comprises a state machine.

32. (New) The device as claimed in claim 30, wherein said stream of data comprises a plurality of data packets and said plurality of portions of data occur within a packet.

33. (New) The device as claimed in claim 16, wherein said stream of data comprises a plurality of data packets and said plurality of portions of data occur within a packet.

34. (New) The device as claimed in claim 33, wherein each portion of data comprises a byte of data.

35. (New) The device as claimed in claim 33, wherein means are provided for identifying which of said plurality of data packets comprise data to be output by said output means.

36. (New) The device as claimed in claim 16, wherein storage means are provided for storing information for each portion of a packet indicating if the portion of data is valid or invalid.